

JOB TITLE: FPGA Design Engineer
RESPONSIBLE TO: R&D Manager
SALARY: Commensurate with experience.

MAIN PURPOSE AND SCOPE OF ROLE:

To work as part of a small flexible embedded systems design team located in Kent, primarily focused on High Definition video conversion systems based around our proprietary CORIO[®] technology.

Primarily responsible for the design and completion of FPGA firmware used throughout our video products.

Production of design concept documentation and continuous code documentation linked with peer reviews.

Participation in design reviews, and to assist in ensuring that project targets with regard to cost and timelines are met.

QUALIFICATIONS:

A numerate degree with several years of hands-on electronic hardware experience and related hands-on FPGA design experience.

EXPERIENCE / JOB KNOWLEDGE:

Proven experience of video FPGA design and ideally several years experience in the video industry.

Skills:

- AHDL & VHDL programming.
- Full understanding of the Altera platform architecture.
- In depth knowledge of Quartus II including understanding of the chip planner.
- Understanding of timing paths and influences within a design.
- Construction of FPGA test benches and Simulation.
- Knowledge of DDR2 SDRAM multiport controllers.
- Comfortable with hardware debugging and a good understanding of schematics.
- Used to working with highly modular based designs.

Experience of complete design cycle from concept to qualification and production release, including test and product qualification.

Ability to work solo or in small teams to create creative solutions for controlling video products.

Ability to discuss ideas and develop solutions with other members of the R&D team.